

FB72 - <offline>

"RTU_MB_MASTER"

Nome: MASTER

Famiglia: MODBUS

Autore: VIPA

Versione: 1.3

Versione blocco: 2

Data e ora Codice: 04/02/2014 16.32.32

Interfaccia: 28/01/2014 13.34.11

Lunghezze (blocco / codice / dati): 05000 04312 00070

Protezione KNOW HOW

Nome	Tipo di dati	Indirizzo	Valore iniziale	Commento
IN		0.0		
REQ	Bool	0.0	FALSE	new modbus request with rising edge
HARDWARE	Byte	1.0	B#16#0	1=SLIO CP040 / 2=Speed7 CPU
LADDR	Int	2.0	0	logical address for SLIO CP040
MB_UNIT_ID	Byte	4.0	B#16#0	modbus: unit identifier
MB_FUNCTION	Byte	5.0	B#16#0	modbus: function code
MB_DATA_ADDR	Word	6.0	W#16#0	modbus: starting address or sub function code
MB_DATA_LEN	Int	8.0	0	modbus: quantity
MB_DATA_PTR	Any	10.0		modbus: data (memory or data block)
OUT		0.0		
DONE	Bool	20.0	FALSE	request done without an error
BUSY	Bool	20.1	FALSE	request is in progress
ERROR	Bool	20.2	FALSE	request done with an error, see STATUS for further informations
STATUS	Word	22.0	W#16#0	code description see the manual
IN_OUT		0.0		

Blocco: FB72

History:

V1.4, 40.02.2014: Aufrufe SFC216,217,218 ersetzt durch FC216,217,218

V1.3, 28.01.2014: Interne Timeout-Überwachung aktiviert

V1.2, 16.12.2013: Schutz Modbus Library überarbeitet

V1.1, 21.10.2013: Implementierung Library Schutz

V1.0, 14.10.2013: Freigabeversion